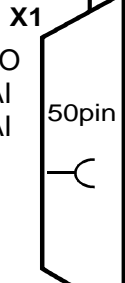


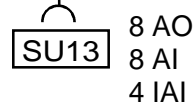
SM500

8 anal. outputs. AO
8 analog inputs AI
4 analog inputs IAI
(integrating V/F)

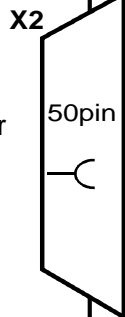


$l=2m$
SC63

(50 pin)



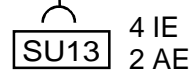
4 increm encoder
2 abs. encoder



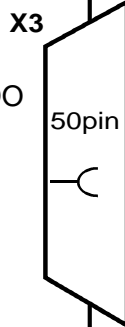
SC63

(50 pin)

$l=2m$



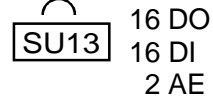
16 dig. outputs DO
16 dig. inputs DI
2 abs. encoders



SC63

(50 pin)

$l=2m$



or

SC62

$l=5*2m$

A B C D (each, 10pin) E

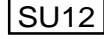
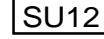
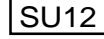
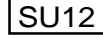
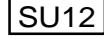
8 DO

8 DO

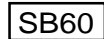
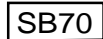
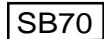
8 DI

8 DI

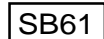
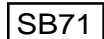
2 AE



or



or



or

